

Unveiling the Industry's Most Comprehensive Custom ASIC Offering

Kevin O'Buckley General Manager of ASIC Business Unit July 2020

Overview

 Company founded
 FY20 revenue

 1995
 \$2.7B

 Employees
 Patents worldwide

 5,000+
 10,000+

Located in Santa Clara, CA R&D centers in US, Israel, India, Germany, China



Marvell's Mission

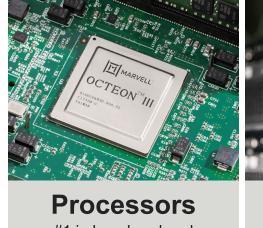
We develop and deliver semiconductor solutions that move, store, process and secure the world's data faster and more reliably than anyone else.



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Marvell offers

the most complete data infrastructure portfolio



#1 in baseband and #1 in HDD and SSD data plane processors controllers, Fibre Channel

> Security #1 in security processors

Storage

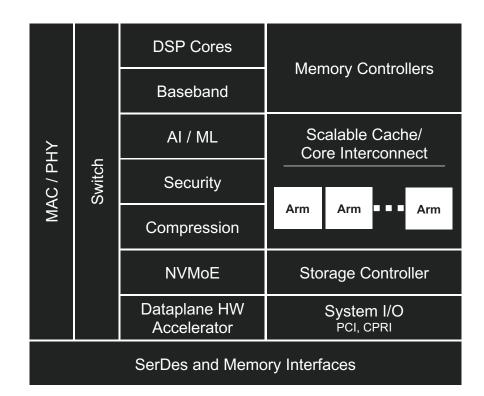


Networking

#2 in Switches and PHYs



Industry-leading data infrastructure IP portfolio



Acquired custom ASIC leader Avera

Becoming one-stop data infrastructure semiconductor partner



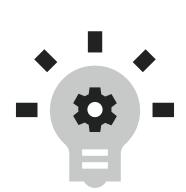
Avera brings 25-year custom ASIC track record



Combination accelerates innovation and capabilities

Best-in-class standard product IP portfolio and scale

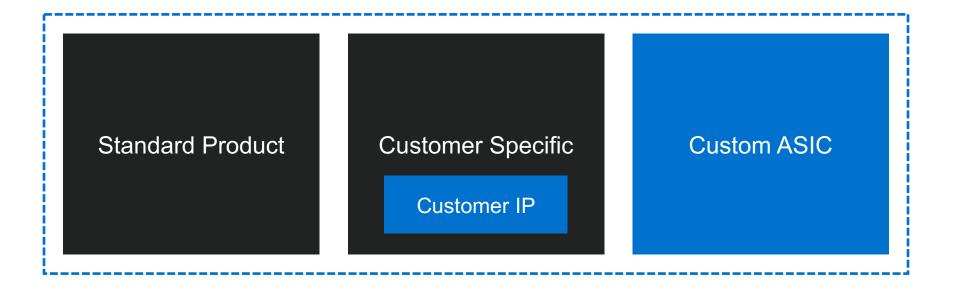




Optimized IPs and technologies in leading-node geometries 25 years of custom ASIC execution

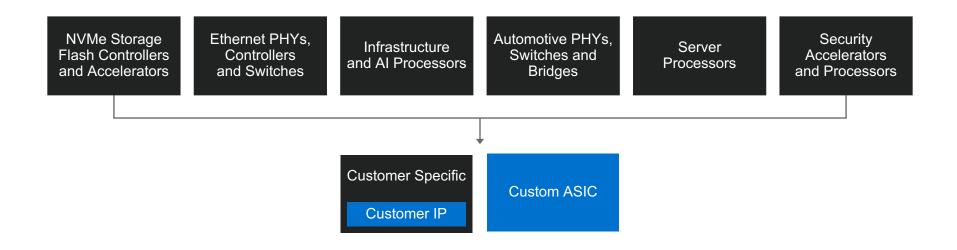


Unveiling industry's most comprehensive offering



Standard products to custom ASICs with everything in between

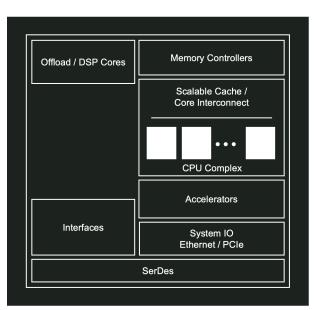
Leveraging best-in-class standard product portfolio



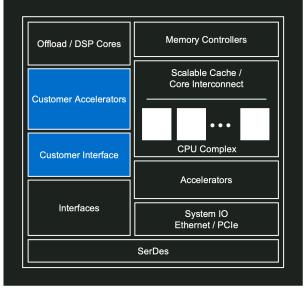
Marvell Standard Products (ASSP)

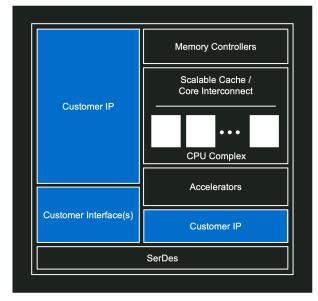
Marvell Customer Specific Standard Product (CSSP)

Custom ASIC solution using optimized Marvell IP



Customer

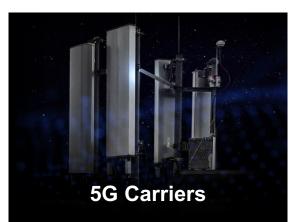




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Marvell

Enabling custom silicon across multiple markets







Marvell Custom ASIC for Wireless Networking



Advanced thermal package Minimum form factor

Massive on chip interconnect solutions

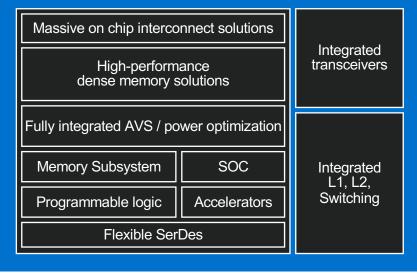
High-performance dense memory solutions

Fully integrated AVS / power optimization

Enhanced capability



Advanced thermal package Minimum form factor Multi-chip / Chiplet based packaging



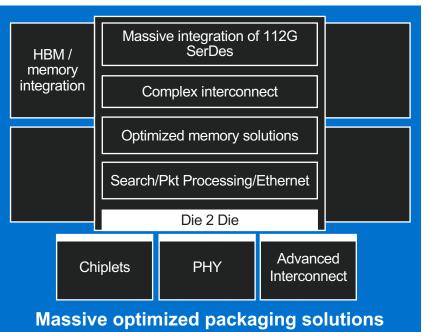
Marvell Custom ASIC for Wired Networking





I		
HBM / memory integration	Massive integration of 56G SerDes	
	Complex interconnect	
	Optimized memory solutions	

Enhanced capability

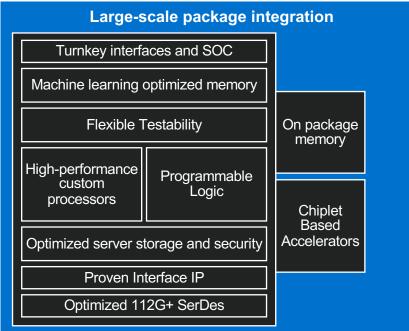


Marvell Custom ASIC for Cloud Data Centers

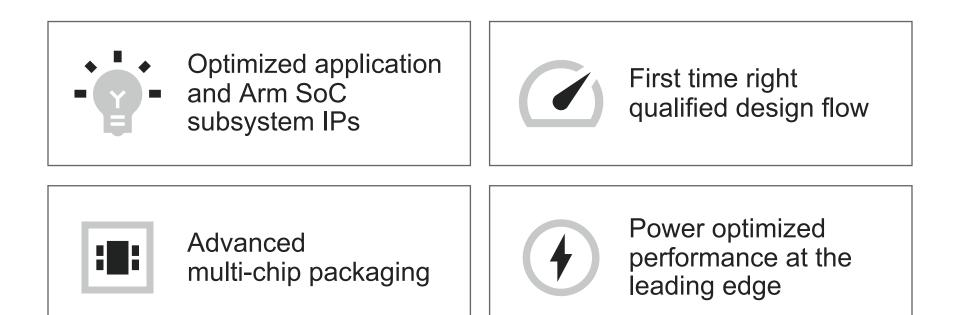


Larga apple pockage integration			
Large-scale package integration			
Turnkey interfaces and SOC			
Machine learning optimized memory	On package memory		
Flexible Testability			

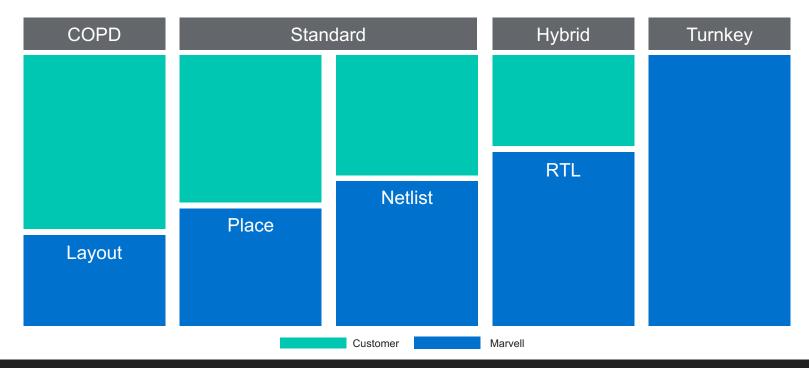




Critical Custom ASIC Technology Differentiators



Flexible custom ASIC engagement business models

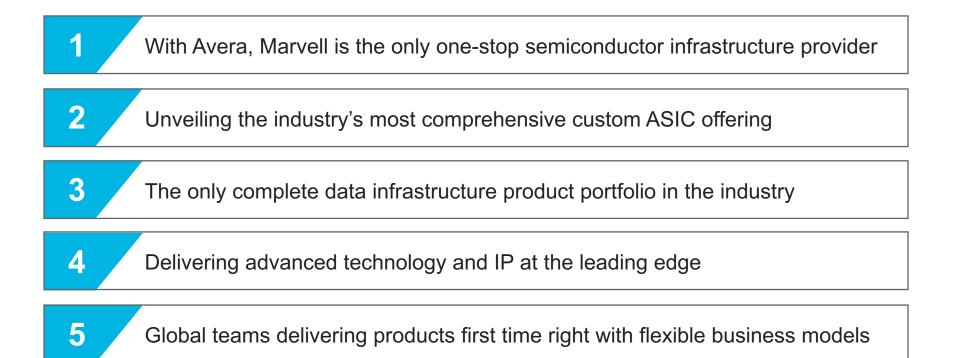


Close collaboration from design architecture start to tapeout release

Global engineering capability



Key takeaways









Essential technology, done right[™]